# JUN TRADENTE

# Corres. and Mail

61 AF/2879

AMENDMENT	TRANSMITTAL	LETTER

ATTORNEY'S DOCKET NO. 01-234

SERIAL NO.	FILING DATE	EXAMINER	GROUP ART ED
10/006,398	November 30, 2001	Layla G. Lauchman noc	38777 2004

INVENTION

**TOTAL** 

INDEP.

ALIGNMENT PROCESS FOR INTEGRATED CIRCUIT STRUCTURES ON SEMICONDUCTOR SUBSTRATE USING SCATTEROMETRY MEASUREMENTS OF LATENT IMAGES IN SPACED APART TEST FIELDS ON SUBSTRATE

## TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:

RESPONSE UNDER 37 C.F.R. 1.16 -EXPEDITED PROCEDURE EXAMINING GROUP 2877

Transmitted herewith is an amendment in the above-identified application.

Small entity status of this application under 37 CAR 1.27 has been established by a verified statement previously submitted.

A verified statement to establish small entity status under 37 CAR 1.9 and 1.27 is enclosed.

X No additional fee is required.

The fee has been calculated as shown below:

(1)

CLAIMS REMAINING

AFTER AMENDMENT (2)

20

4

HIGHEST NO.

PREVIOUSLY PAID FOR (3)

PRESENT EXTRA

0

0

RATE	ADDIT. FEE
x \$ 9 =	\$
x \$ 43 =	\$
+ \$145 =	\$
TOTAL ADDIT. FEE	\$

SMALL ENTITY

OTHER THAN A SMALL ENTITY

<u>OR</u>	RATE	ADDIT. FEE		
	x \$ 18 =	\$	0	
	x \$ 86 =	\$	0	
	+ \$290 =	\$	O	
<u>OR</u>	TOTAL	\$	0	

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FIRST PRESENTATION OF MULTIPLE DEP. CLAIM

19

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	harge Deposit Account Noate copy of this sheet is enclosed.	in the amount of	f\$
A check	in the amount of \$	to cover the filir	ng fee is enclosed.
	mmissioner is hereby authorized to charge payment of ment to Deposit Account No. 12-2252		
	Any additional filing fees required under 37 CAR 1.16.		
	Any patent application processing fees under 37 CAR 1	.17.	D-71.
June 9,	2004	John	1. Jayeon
(Date)		//	(Signature)

<sup>\*\*</sup> If the "Highest No. Previously Paid For" IN THIS SPACE is less than 20, enter "20".

<sup>\*\*\*</sup> If the "Highest No. Previously Paid For" IN THIS SPACE is less than 3, enter "3".

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# UNDER 37 C.F.R. 1.116-XAMINE GROUP 2877

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Appl. No.

10/006,398

ന Corres. and Mail

Applicants:

Colin D. Yates, Nicholas F. Pasch, and Nicholas K. Eib

Filed:

November 30, 2001

Title

ALIGNMENT PROCESS FOR INTEGRATED CIRCUIT STRUCTURES ON SEMICONDUCTOR SUBSTRATE USING SCATTEROMETRY MEASUREMENTS OF LATENT IMAGES IN SPACED APART TEST

FIELDSMON SUBSTRATE

Grp./ A.U.

2877

Examiner

Layla G. Lauchman

Docket No.

01-234

#### CERTIFICATE OF MAILING

I hereby certify that this amendment is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

June 9, 2004

John P. Taylor, Reg. No. 22,369

June 9, 2004

### **RESPONSE TO FINAL REJECTION**

**Honorable Commissioner for Patents** Post Office Box 1450 Alexandria, VA 22313-1450

Date: June 9, 2004

Sir:

This is in response to the Final Rejection mailed April 23, 2004.